

Abstract of the Disclosure

A reset signal generating circuit and a nonvolatile ferroelectric memory device using the same are disclosed.

5 The reset signal generating circuit comprises: a power detector for maintaining the size of an applied voltage for a predetermined period; a threshold voltage controller for outputting a voltage by regulating the level of a power voltage for generating a reset signal depending on
10 variations of the power voltage and a bias voltage; a feedback controller for pulling down an output voltage of the power detector when the power voltage reaches a predetermined level depending on an output voltage of the threshold voltage controller; a pull-up controller for
15 pulling up an output voltage of the power detector and outputting an output voltage variation of the power detector as the reset signal; and a self-bias unit for outputting the bias voltage and regulating the amount of a current supplied from the threshold voltage controller to
20 the feedback controller depending on variations of the power voltage. As a result, a reset signal is stably generated only when the power voltage is beyond a predetermined level regardless of a power-up slope.